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	550-241	09/887,522
(Use several sheets if necessary)	APPLICANT NEVILL et al.	
	FILING DATE June 25, 2001	GROUP

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JSO	6,012,138	1/2000	Worrell			

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
JSO	2 289 354	11/1995	UK				

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

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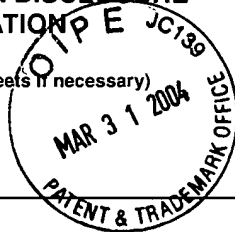
Form PTO-FB-A820 (Also PTO-1449)

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BSO	5,802,373	9/1998	Yates et al			

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
BSO	0 251 716	1/1988	EP				

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

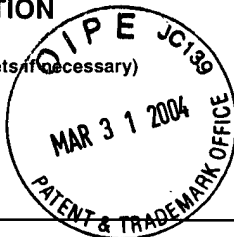| *Examiner	Date Considered
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BSO	5,581,718	12/1996	Grochowski	—	—	
BSO	5,898,885	4/1999	Dickol et al	—	—	
BSO	5,909,567	6/1999	Novak et al	—	—	

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FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
BSO	WO 99/27439	6/1999	WIPO	—	—		
BSO	WO 00/33180	6/2000	WIPO	—	—		
BSO	WO 00/34844	6/2000	WIPO	—	—		
BSO	0 718 758	6/1996	EP	—	—		
BSO	0 772 122	5/1997	EP	—	—		

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

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	Applicant **NEVILL**	
(Use several sheets if necessary)	Filing Date **June 25, 2001**	Group **2183**

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BJ6	3,889,243	6/1975				
	4,236,204	11/1980				
	4,587,632	5/1986				
	4,969,091	11/1990				
	4,922,414	5/1990				
	5,136,696	8/1992				
	5,455,775	10/1995				
	5,619,665	4/1997				
	5,638,525	6/1997				
	5,659,703	8/1997				
	5,740,461	4/1998				
	5,742,802	4/1998				
	5,752,035	5/1998				
	5,784,584	7/1998				
	5,809,336	9/1998				
	5,838,948	11/1998				
	5,875,336	2/1999				
	5,892,966	4/1999				
	5,925,123	7/1999				
	5,926,832	7/1999				
	5,937,193	8/1999				
	5,953,741	9/1999				
	6,003,126	12/1999				
	6,009,499	12/1999				
	6,009,509	12/1999				
	6,014,723	1/2000				
	6,021,469	2/2000				
	6,026,485	2/2000				
	6,031,992	2/2000				
	6,038,643	3/2000				
	6,070,173	5/2000				
	6,088,786	7/2000				
	6,122,638	9/2000				
	6,125,439	9/2000				
	6,148,391	11/2000				
	6,298,434	10/2001				
	6,317,872	11/2001				
	6,338,134	1/2002				
	6,349,377	2/2002				
	6,374,286	4/2002				
	6,606,743	8/2003				

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*Examiner		Date Considered	6/22/04

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550-241	09/887,522

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

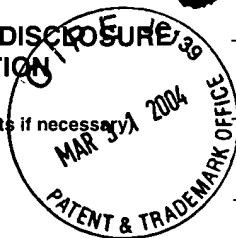|		
550	42	H. Stone, Chapter 12 - "A Pipeline Push-Down Stack Computer", 1969, pages 235-249
	43	C. Glossner et al, "Delft-Java Link Translation Buffer", 8/1998
	44	N. Vijaykrishnan et al, "Object-Oriented Architectural Support For a Java Processor" 1998, pages 330-355
	45	C. Glossner et al, "The Delft-Java Engine: An Introduction", 8/1997
	46	K. Ebcioglu et al, "A Java ILP Machine Based On Fast Dynamic Compilation", 1/1997, pages 1-13
	47	A. Wolfe, "First Java-specific chip takes wing" *EETimes* - 1997
	48	Y. Patt, *Introduction to Computer Systems From Bits and Gates to C and Beyond*, 1999, pages 1-517
	49	M. Ertl, "Stack Caching for Interpreters" 1994, pages 1-13
	50	M. Ertl, "Stack Caching for Interpreters" 1995, pages 1-13
	51	M. Ertl, "Implementation of Stack-Based Languages on Register Machines" 4/1996, pages 1-4
	52	J. O'Connor et al, "PicoJava-I: The Java Virtual Machine in Hardware" *IEEE Micro* A Case for Intelligent RAM, March/April 1997, pages 45-53
	53	K. Andrews et al, "Migrating a CISC Computer Family Onto RISC Via Object Code Translation" 1992, pages 213-222
	54	"PicoJava I Microprocessor Core Architecture" 10/1996, pages 1-8, Sun Microsystems
	55	M. Ertl, "A New Approach to Forth Native Code Generation" 1992
	56	M. Maierhofer et al, "Optimizing Stack Code" 1997, page 19
	57	D. Ungar et al, "Architecture of SOAR: Smalltalk on a RISC" The 11th Annual International Symposium on Computer Architecture, 6/1984, pages 188-197
	58	O. Steinbusch, "Designing Hardware to Interpret Virtual Machine Instructions" 2/1998, pages 1-59
	59	R. Kapoor et al, "Stack Renaming of the Java Virtual Machine" 12/1996, pages 1-17
	60	A. Yonezawa et al, "Implementing Concurrent Object-Oriented Languages in Multicomputers" *Parallel and Distributed Technology (Systems and Applications)* 5/1993, pages 49-61
	61	C. Hsieh et al, "Java Bytecode to Native Code Translation; The Caffeine Prototype and Preliminary Results" IEEE/ACM International Symposium on Microarchitecture, 12/1996, pages 90-97
	62	Y. Patt et al, *Introduction to Computer Systems From Bits and Gates to C and Beyond*, 2001, pages 1-526
	63	Sun Microsystems PicoJava Processor Core Data Sheet, 12/1997, pages 1-11
	64	H. McGhan et al, PicoJava A Direct Execution Engine for Java Bytecode, 10/1998, pages 22-26
	65	C. Glossner et al, "Parallel Processing" Euro-Par 1997: Passau, Germany, 8/1997
	66	Y. Patt, *Introduction to Computer Systems From Bits and Gates to C and Beyond*, 1999, pages 10-12 & 79-82
	67	Espresso - The High Performance Java Core Specification, 10/2001, pages 1-33, Aurora VLSI, Inc.
	68	J. Gosling, "Java Intermediate Bytecodes" 1995, pages 111-118
	69	P. Koopman, Jr. "Stack Computers The New Wave" 1989, pages 1-234
	70	M. Mrva et al, "A Scalable Architecture for Multi-Threaded JAVA Applications" Design Automation and Test in Europe, 2/1998, pages 868-874
	71	L. Chang et al, "Stack Operations Folding in Java Processors" *IEEE Proc. - Comput. Digit. Tech.*, Vol. 145, No. 5, pages 333-340 9/1998
	72	L. Ton et al, Proceedings of the '97 International Conference on Parallel and Distributed Systems, "Instruction Folding in Java Processor", pages 138-143, 12/1997

*Examiner		Date Considered	6/22/04

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	Applicant NEVILL	
	Filing Date June 25, 2001	Group 2183

550	73	K. Buchenrieder et al, "Scalable Processor Architecture for Java With Explicit Thread Support" *Electronics Letters* Vol. 33, No. 18, pages 1532+, 8/1997
	74	C. Chung et al, Proceedings of the '98 International Conference on Parallel and Distributed Systems, "A Dual Threaded Java Processor for Java Multithreading" pages 693-700, 12/1998
	75	I. Kazi et al, "Techniques for Obtaining High Performance in Java Programs" 9/2000, pages 213-240
	76	R. Kieburtz, "A RISC Architecture for Symbolic Computation" 1987, pages 146-155
	77	M. Berekovic et al, "Hardware Realization of a Java Virtual Machine for High Performance Multimedia Applications" *Signal Processing Systems SIPS 98*, pages 479-488, 1997
	78	P. Deutsch, "Efficient Implementation of the Smalltalk-80 System" 1983, pages 297-302
	79	"Rockwell Produces Java Chip" 9/1997, CNET NEWS.COM
	80	Y. Patt et al, *Introduction to Computing Systems from Bits and Gates to C and Beyond*, 2001, pages 1-16, 91-118 & 195-209
	81	
	82	
	83	

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